

In the Claims:

Please amend the claims as follows:

1. (Currently Amended) A programmable circuit operable to:
receive multiple versions of firmware from an external source, ~~the~~ each version of firmware representing a corresponding operating configuration;
store the multiple versions of firmware in a memory; and
download a selected one of the versions of firmware from the memory.
2. (Currently Amended) The programmable circuit of claim 1, further operable to operate in the operating configuration corresponding to the downloaded version of firmware after downloading ~~the~~ this version of firmware from the memory.
3. (Original) The programmable circuit of claim 1 wherein the memory comprises a nonvolatile memory.
4. (Original) The programmable circuit of claim 1 wherein the memory comprises an external memory.
5. (Currently Amended) A programmable circuit operable to:
download from a memory storing a plurality of versions of firmware a first firmware version that represents a first configuration;
operate in the first configuration;
download from the memory a second firmware version that represents a second configuration; and
operate in the second configuration.
6. (Currently Amended) The programmable circuit of claim 5 wherein the programmable circuit is further operable to:

receive the second firmware version from an external source while operating in the first configuration; and

storing the second firmware version in the memory while operating in the first configuration, and wherein the second firmware version may only be received when operating in the first configuration.

7. (Currently Amended) A programmable-circuit unit, comprising:
a memory; and

a programmable circuit coupled to the memory and operable to,

receive multiple versions of firmware from an external source, ~~the each~~
version of firmware representing a corresponding operating configuration of the
programmable circuit,

store each of the versions of firmware in the memory, and

download a selected one of the firmware versions from the memory.

8. (Original) The programmable-circuit unit of claim 7 wherein the memory
comprises an electrically erasable and programmable read-only memory.

9. (Original) The programmable-circuit unit of claim 7 wherein the
programmable circuit comprises a field-programmable gate array.

10. (Currently Amended) A programmable-circuit unit, comprising:

a memory operable to store ~~first and second~~ a plurality of versions of firmware data,
each version that respectively representing a corresponding first and second configurations
of the programmable-circuit unit; and

a programmable circuit coupled to the memory and operable to,

download ~~the~~ a first selected one of the versions of first-firmware data from
the memory,

operate in the ~~first-configuration~~ corresponding to the first selected version of
firmware data,

download ~~the~~ a second selected on of the versions of firmware data from the
memory, and

operate in the ~~second~~ configuration corresponding to the second selected version of firmware data.

11. (Original) The programmable-circuit unit of claim 10 wherein the programmable circuit is further operable to:

receive the ~~second~~ selected version of firmware data from an external source while operating in the first configuration; and

store the ~~second~~ selected version of firmware data in the memory while operating in the first configuration, and wherein the second selected firmware version of firmware may only be received and stored when operating in the configuration corresponding to the first selected version of firmware data.

12. (Original) The programmable-circuit unit of claim 10 wherein the programmable circuit is operable to load the second firmware while operating in the first configuration.

13. (Currently Amended) A programmable-circuit unit, comprising:

a memory operable to store ~~first, second, third, and fourth~~ a plurality of firmware configurations, each firmware configuration that respectively representing a corresponding first, second, third, and fourth operational configurations;

a first programmable circuit coupled to the memory and operable to,

download the ~~a~~ selected one of the first-firmware configurations from the memory,

operate in the ~~first~~ operational configuration corresponding to the selected firmware configuration,

download the ~~second~~ a different one of the firmware configurations from the memory, and

operate in the ~~second~~ operational configuration corresponding to the different firmware configuration; and

a second programmable circuit coupled to the memory and to the first programmable circuit and operable to,

download the ~~a~~ selected one of the third firmware configurations data from the

memory,

operate in the ~~third~~operational configuration corresponding to the selected one of the firmware configurations,

download the ~~fourth~~a different one of the -firmware configurations from the memory, and

operate in the ~~fourth~~operational configuration corresponding to the different firmware configuration.

14. (Original) The programmable-circuit unit of claim 13 wherein the first programmable circuit is further operable to:

receive the ~~second and fourth~~different ones of the firmware configurations from an external source while operating in the first configuration; and

store the ~~second and fourth~~different ones of the firmware configurations in the memory while operating in the first configuration.

15. (Original) The programmable-circuit unit of claim 13 wherein the first and second programmable circuits comprise respective field-programmable gate arrays.

16. (Currently Amended) A computing machine, comprising:

a processor; and

a programmable-circuit unit coupled to the processor and comprising,

a memory stores a plurality of firmware configurations for the programmable-circuit unit, and

a programmable circuit coupled to the memory and operable to,

receive from the processor a new firmware configuration that represents a new configuration of the programmable circuit,

store the new firmware configuration in the memory, and

download the new firmware configuration from the memory in response to the processor.

17. (Currently Amended) The computing machine of claim 16 wherein the processor is operable to:

before sending the new firmware configuration to the programmable circuit, determine whether the new firmware configuration is already stored in the memory; and

send the new firmware configuration to the programmable circuit only if the firmware is not already stored in the memory.

18. (Currently Amended) The computing machine of claim 16, further comprising:

a configuration registry coupled to the processor and operable to store the new firmware configuration and to indicate that the firmware configuration represents a desired configuration for the programmable circuit; and

wherein the processor is operable to download the firmware configuration from the configuration registry to the programmable circuit.

19. (Original) The computing machine of claim 16, wherein:

the programmable-circuit unit comprises a pipeline unit; and

the programmable circuit includes a hardwired pipeline that is operable to operate on data.

20. (Currently Amended) A computing machine, comprising:

a processor; and

programmable-circuit unit coupled to the processor and comprising,

a memory operable to store ~~first and second~~ a plurality of firmware versions that respectively represent ~~first and second configurations~~ configuration of the programmable-circuit unit; and

a programmable circuit operable to,

download ~~the first~~ a selected one of the firmware versions from the memory,

operate in the ~~first~~ configuration corresponding to the downloaded firmware version,

download ~~the second~~ a different firmware version from the memory in response to the processor, and

operate in the ~~second~~-configuration corresponding to the different firmware version.

21. (Currently Amended) The computing machine of claim 20 wherein:
the processor comprises a first test port;
the programmable-circuit unit comprise a second test port that is coupled to the first test port; and

the processor is operable to load the ~~first~~-selected one of the firmware versions into memory via the first and second test ports.

22. (Currently Amended) The computing machine of claim 20 wherein:
the processor comprises a first test port;
the programmable-circuit unit comprises a second test port that is coupled to the first test port;

while operating in the ~~first~~-configuration corresponding to the selected one of the firmware versions, the programmable circuit is operable to perform a self test and to provide self-test data to the processor via the first and second test ports; and

the processor is operable to cause the programmable circuit to download the ~~second~~-different firmware version from memory only if the self-test data indicates a predetermined result of the self test.

23. (Currently Amended) The computing machine of claim 20 wherein:
the processor is operable to send the ~~second~~-selected one of the firmware versions firmware to the programmable circuit; and

while operating in the ~~first~~-configuration corresponding to the selected one of the firmware versions, the programmable circuit is operable to load the ~~second~~-different firmware version into the memory in response to the processor.

24. (Currently Amended) A computing machine, comprising:
a processor; and

programmable-circuit unit coupled to the processor and comprising,

a memory operable to store ~~first, second, third, and fourth~~ a plurality of

firmware codes that respectively represent ~~first, second, third, and fourth~~ configurations,

a first programmable circuit coupled to the memory and operable to,

download the a first firmware code from the memory,

operate in ~~the~~ a first configuration corresponding to the first firmware code,

download ~~the~~ a second firmware code from the memory in response to the processor, and

operate in ~~the~~ a second configuration corresponding to the second firmware code, and

a second programmable circuit coupled to the memory and to the first programmable circuit and operable to,

download ~~the~~ a third firmware code from the memory,

operate in ~~the~~ a third configuration corresponding to the third firmware code,

download ~~the~~ a fourth firmware code from the memory in response to the processor, and

operate in ~~the~~ a fourth configuration corresponding to the fourth configuration code.

25. (Currently Amended) The computing machine of claim 24 wherein:
the processor comprises a first test port;
the programmable-circuit unit comprise a second test port that is coupled to the first test port; and
the processor is operable to load the first and third firmware codes into memory via the first and second test ports.

26. (Currently Amended) The computing machine of claim 24 wherein:
the processor comprises a first test port;
the programmable-circuit unit comprises a second test port that is coupled to the first test port;

while operating in the first configuration, the first programmable circuit is operable to perform a first self test and to provide first self-test data to the processor via the first and second test ports;

while operating in the third configuration, the second programmable circuit is operable to perform a second self test and to provide second self-test data to the processor via the first and second test ports; and

the processor is operable to cause the first and second programmable circuits to respectively load the second and fourth firmware codes from the memory only if the first and second self-test data indicate respective predetermined results of the first and second self tests.

27. (Currently Amended) The computing machine of claim 24 wherein:

the processor is operable to send the second and fourth firmware codes to the first programmable circuit; and

while operating in the first configuration, the first programmable circuit is operable to load the second and fourth firmware codes into the memory in response to the processor.

28. (Currently Amended) The computing machine of claim 24 wherein the memory comprises:

a first memory section coupled to the first programmable circuit and operable to store the first and second firmware codes; and

a second memory section coupled to the first and second programmable circuits and operable to store the third and fourth firmware codes.

29. (Original) The computing machine of claim 28 wherein the first and second memory sections are respectively disposed on first and second integrated circuits.

30. (Currently Amended) A method, comprising:

providing a plurality of firmware codes to a programmable circuit, ~~the~~ each firmware code representing a configuration of the circuit;

storing each of the ~~configuration data~~ firmware codes in a memory with the programmable circuit; and

downloading a selected one of the configuration data~~firmware codes~~ from the memory into the programmable circuit.

31. (Original) The method of claim 30, further comprising operating in the configuration after downloading the configuration data from the memory.

32. (Currently Amended) A method, comprising:
storing memory a plurality of firmware codes, each firmware code representing a configuration of a programmable circuit;
downloading into ~~a~~the programmable circuit a first firmware code that represents a first configuration;
operating the programmable circuit in the first configuration;
downloading into the programmable circuit second firmware that represents a second configuration; and
operating the programmable circuit in the second configuration after downloading the second firmware.

33. (Currently Amended) The method of claim 32 wherein downloading the second firmware code comprises:
sending the second firmware code to the programmable circuit;
loading the second firmware code into ~~a~~the memory with the programmable circuit while the programmable circuit is operating in the first configuration; and
downloading the second firmware code from the memory into the programmable circuit.

34. (Currently Amended) The method of claim 32 wherein downloading the second firmware code comprises:
determining whether the second firmware code is stored in ~~a~~the memory coupled to the programmable circuit;
sending the second firmware code to the programmable circuit only if the second firmware code is not stored in the memory;

loading the second firmware code into the memory with the programmable circuit while the programmable circuit is operating in the first configuration; and
downloading the second firmware code from the memory into the programmable circuit.

35. (Currently Amended) The method of claim 32 wherein:
operating the programmable circuit in the first configuration comprises testing the programmable circuit; and
downloading the second firmware code comprises downloading the second firmware code only if the programmable circuit passes the testing.

36. (Currently Amended) A method, comprising:
storing in memory a plurality of firmware codes, each firmware code defining a corresponding operational configuration for one of first and a second programmable circuit;
downloading a first and a second one of the firmware codes into the first and second programmable circuits, respectively;
operating the first and second programmable circuits in the first and second operational configurations, respectively;
downloading a third and a fourth firmware code into the first and second programmable circuits, respectively, via the first programmable circuit; and
operating the first and second programmable circuits in the third configuration and fourth configurations, respectively.

37. (Currently Amended) The method of claim 36 wherein downloading the first and second ~~firmware codes~~ comprises downloading the first and second firmware codes into the first and second programmable circuits, respectively, via a test port.

38. (Currently Amended) The method of claim 36 wherein:
operating the first and second programmable circuits in the first and second configurations comprises testing the first and second programmable circuits; and
loading the third and fourth firmware codes into the first and second programmable circuits comprises,

loading the third firmware code only if the testing indicates that the first programmable circuit is functioning as desired, and
loading the fourth firmware code only if the testing indicates that the second programmable circuit is functioning as desired.